General purpose (dual digital transistors)

EMD6 / UMD6N / IMD6A

Features

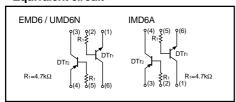
- 1) Both the DTA143T chip and DTC143T chip in an EMT or UMT or SMT package.
- 2) Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

Structure

A PNP and NPN digital transistor (each with a single built in resistor)

The following characteristics apply to both the DTr1 and DTr2, however, the "-" sign on DTr2 values for the PNP type have been omitted.

●Equivalent circuit

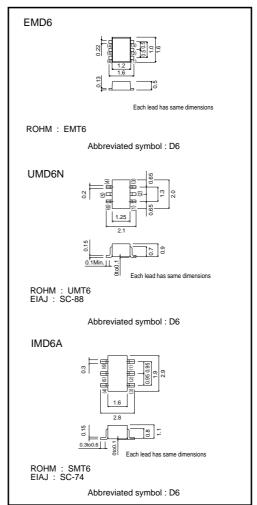


● Absolute maximum ratings (Ta = 25°C)

Pa	arameter	Symbol	Limits	Unit			
Collector-base voltage		Vсво	50	V			
Collector-emitter voltage		Vceo	50	V			
Emitter-base voltage		VEBO	5	V			
Collector current		lc	100	mA			
Collector power dissipation	EMD6, UMD6N	Pc	150 (TOTAL)	*1 mW *2			
	IMD6A	PC	300 (TOTAL)				
Junction temperature		Tj	150	°C			
Storage temperature		Tstg	-55~+150	°C			

- *1 120mW per element must not be exceeded. *2 200mW per element must not be exceeded.

●External dimensions (Units : mm)



●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Collector-base breakdown voltage	ВУсво	50	_	_	V	Ic=50μA	
Collector-emitter breakdown voltage	BVceo	50	_	_	V	Ic=1mA	
Emitter-base breakdown voltage	ВУево	5	_	_	٧	I _E =50μA	
Collector cutoff current	Ісво	_	_	0.5	μΑ	VcB=50V	
Emitter cutoff current	ІЕВО	-	-	0.5	μА	V _{EB} =4V	
Collector-emitter saturation voltage	VCE (sat)	_	_	0.3	V	Ic/Iв=5mA/0.25mA	
DC current transfer ratio	hfe	100	250	600	_	VcE=5V, Ic=1mA	
Transition frequency	f⊤	_	250	_	MHz	Vc==10V, I==-5mA, f=100MHz *	
Input resistance	R ₁	3.29	4.7	6.11	kΩ	_	

st Transition frequency of the transistor

Packaging specifications

	Package		Taping	
	Code	T2R	TR	T108
Туре	Basic ordering unit (pieces)	8000	3000	3000
EMD6		0	_	_
UMD6N		_	0	_
IMD6A		_	_	0

•Electrical characteristic curves

DTr₁ (NPN)

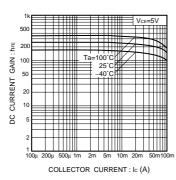


Fig.1 DC current gain vs. collector current

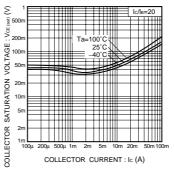


Fig.2 Collector-emitter saturation voltage vs. collector current

DTr₂ (PNP)

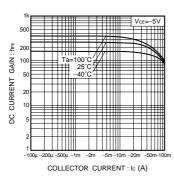


Fig.3 DC current gain vs. collector current

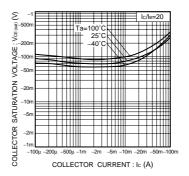


Fig.4 Collector-emitter saturation voltage vs. collector current

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